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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/562,276	12/22/2005	David A. Fish	GB030102	6568
24737	7590	10/13/2010	EXAMINER	
PHILIPS INTELLECTUAL PROPERTY & STANDARDS			PIZIALI, JEFFREY J	
P.O. BOX 3001			ART UNIT	PAPER NUMBER
BRIARCLIFF MANOR, NY 10510			2629	
MAIL DATE	DELIVERY MODE			
10/13/2010	PAPER			

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Advisory Action Before the Filing of an Appeal Brief</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	10/562,276	FISH ET AL.
	<b>Examiner</b>	<b>Art Unit</b>
	Jeff Piziali	2629

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 08 October 2010 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1.  The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

- a)  The period for reply expires \_\_\_\_\_ months from the mailing date of the final rejection.
- b)  The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  
Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### NOTICE OF APPEAL

2.  The Notice of Appeal was filed on \_\_\_\_\_. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

#### AMENDMENTS

3.  The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because
- (a)  They raise new issues that would require further consideration and/or search (see NOTE below);
  - (b)  They raise the issue of new matter (see NOTE below);
  - (c)  They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
  - (d)  They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: See Continuation Sheet. (See 37 CFR 1.116 and 41.33(a)).

4.  The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).
5.  Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.
6.  Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
7.  For purposes of appeal, the proposed amendment(s): a)  will not be entered, or b)  will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: \_\_\_\_\_.

Claim(s) objected to: \_\_\_\_\_.

Claim(s) rejected: 16-29.

Claim(s) withdrawn from consideration: 1-14.

#### AFFIDAVIT OR OTHER EVIDENCE

8.  The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).
9.  The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).
10.  The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

#### REQUEST FOR RECONSIDERATION/OTHER

11.  The request for reconsideration has been considered but does NOT place the application in condition for allowance because: \_\_\_\_\_.
12.  Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s). \_\_\_\_\_
13.  Other: \_\_\_\_\_.

/Jeff Piziali/  
Primary Examiner, Art Unit 2629  
12 October 2010

Continuation of 3. NOTE:

The Applicant is thanked for the After-Final Amendment (filed on 8 October 2010).

However, if entered, the Applicant's proposed claim amendments would add at least the subject matter:

"compensation circuitry for modifying target pixel drive currents to take account of a voltage at each of said pixel elements and a dependency of a brightness characteristic associated with a corresponding pixel, the compensation circuitry comprising:... means (100,104) for scaling the target drive currents by applying a value, representing the dependency of the brightness characteristic of the corresponding pixel element, on the voltage on a conductor associated with a row containing the corresponding pixel element, said value being determined based on characteristics of the driver circuitry associated with the pixel element" to claim 16;

"the means for applying an algorithm derives values corresponding to the multiplication of a vector of the target pixel drive currents for a row of pixel elements by the inversion of the matrix M,... wherein a number of rows and columns of matrix M is equal to a number of pixel elements in a row" to claim 17;

"wherein each pixel element comprises: a current source circuit (22,24) comprising a drive transistor (22) which converts an input voltage to a current and wherein the means for scaling determines the value derived from a current-voltage characteristic of the drive transistor; and a voltage-current characteristic of a corresponding current-addressed light emitting display element" to claim 18;

"the drive transistor (22) and the light emitting display element (2) of each pixel element are in series between the conductor (26) associated with the row containing the corresponding pixel element and a common line" to claim 19;

"the means for scaling the value is further derived from a resistance (R) of the conductor associated with the row containing the corresponding pixel element" to claim 21;

"the means for scaling (100) the value is determined... where: R is the resistance of a conductor between adjacent pixel elements... a ratio of a current drawn by a pixel element during a pixel programming phase to a current drawn by the pixel element during display" to claim 22;

"F(n) is an nth term of a vector result of multiplying the vector of the target pixel drive currents for a row of pixel elements by the inversion of the matrix M, F(0) being the first term" to claim 23; and

"Compensation circuitry for modifying target pixel drive currents for a display device which comprises an active matrix array of current-addressed light emitting pixel elements arranged in rows and columns having a respective row conductor and a column conductor, the compensation circuitry comprising: means (60, 62, 64, 66, 70, 72, 74, 76, 78, 80, 82, 90, 92) for applying an algorithm to the target pixel drive currents which represents a relationship between currents drawn by pixel elements in a row and voltages on a conductor associated with the row at a corresponding location of the pixel elements in the row; and means (100, 104) for scaling the target pixel drive currents by applying a value, representing a dependency of a pixel brightness characteristic, to the voltage on the conductor associated with the row, said value being determined based on characteristics of a driver circuitry associated with a corresponding one of the pixel elements" to claim 27.

The proposed claim amendments, if incorporated into present claim language, would substantially alter inventive scope of the claims, requiring additional search and consideration.

Due to the proposed amendments not being entered, Applicant's arguments are not commensurate in scope with the current claims.

By such reasoning, non-entry of the proposed Amendment (filed on 8 October 2010) is deemed proper and necessary at this time.

/Jeff Piziali/  
Primary Examiner, Art Unit 2629  
12 October 2010